

Name	Severity	Details
Vivado Commands		
General Messages		
Project 1-313	Information	Project file moved from 'C:/Users/chira/Desktop/DAC/CK_DAC_V1' since last save.
filemgmt 56-2	Information	Default IP Output Path : Could not find the directory 'C:/Users/chira/Desktop/DAC/SignalGenerator/CK_DAC_V1.gen/sources_1', nor could it be found using path 'C:/Users/chira/Desktop/DAC/CK_DAC_V1/CK_DAC_V1.gen/sources_1'.
Project 1-90	Status	Scanning sources...
Project 1-47	Status	Finished scanning sources
IP_Flow 19-234	Information	Refreshing IP repositories
IP_Flow 19-1704	Information	No user IP repositories specified
IP_Flow 19-2313	Information	Loaded Vivado IP repository 'C:/Xilinx/Vivado/2022.1/data/ip'.
IP_Flow 19-2162	Warning	IP 'selectio_wiz_0' is locked: * IP 'selectio_wiz_0' does not support the current project part 'xcau25p-ffvb676-2-e'. Note that part differences may result in undefined behavior. * IP definition 'SelectIO Interface Wizard (5.1)' for IP 'selectio_wiz_0' (customized with software release 2017.4.1) has a different revision in the IP Catalog.
IP_Flow 19-2162	Warning	IP 'clk_wiz_0' is locked: * IP 'clk_wiz_0' does not support the current project part 'xcau25p-ffvb676-2-e'. Note that part differences may result in undefined behavior. * IP definition 'Clocking Wizard (5.4)' for IP 'clk_wiz_0' (customized with software release 2017.4.1) has a newer major version in the IP Catalog.

IP_Flow 19-2162	Warning	IP 'cordic_0' is locked: * IP definition 'CORDIC (6.0)' for IP 'cordic_0' (customized with software release 2017.4.1) has a different revision in the IP Catalog. * Current project part 'xcau25p-ffvb676-2-e' and the part 'xc7a75tfgg484-1' used to customize the IP 'cordic_0' do not match.
IP_Flow 19-2162	Warning	IP 'fifo_generator_0' is locked: * IP definition 'FIFO Generator (13.2)' for IP 'fifo_generator_0' (customized with software release 2017.4.1) has a different revision in the IP Catalog. * Current project part 'xcau25p-ffvb676-2-e' and the part 'xc7a75tfgg484-1' used to customize the IP 'fifo_generator_0' do not match.

Synthesis

Out-of-Context Module Runs

cordic_0_synth_1

Vivado_Tcl 4-113	Status	Command: synth_design -top cordic_0 -part xcau25p-ffvb676-2-e -incremental_mode off -mode out_of_context
Vivado_Tcl 4-321	Status	Starting synth_design
Common 17-347	Status	Attempting to get a license for feature 'Synthesis' and/or device 'xcau25p'
Vivado_Tcl 4-41	Status	7 Infos, 1 Warnings, 0 Critical Warnings and 2 Errors encountered.
Vivado_Tcl 4-43	Status	synth_design failed
Vivado_Tcl 4-391	Warning	The following IPs are missing output products for Synthesis target. These output products could be required for synthesis, please generate the output products using the generate_target or synth_ip command before running synth_design. C:/Users/chira/Desktop/DAC/SignalGenerator/HDL/ip/cordic_0/cordic_0.xci

IP_Flow 19-2162	Warning	IP 'cordic_0' is locked: * IP definition 'CORDIC (6.0)' for IP 'cordic_0' (customized with software release 2017.4.1) has a different revision in the IP Catalog. * Current project part 'xcau25p-ffvb676-2-e' and the part 'xc7a75tfgg484-1' used to customize the IP 'cordic_0' do not match.
Common 17-349	Information	Got license for feature 'Synthesis' and/or device 'xcau25p'
Device 21-403	Information	Loading part xcau25p-ffvb676-2-e
Synth 8-7079	Information	Multithreading enabled for synth_design using a maximum of 2 processes.
Synth 8-7078	Information	Launching helper process for spawning children vivado processes
Synth 8-7075	Information	Helper process launched with PID 10452
Synth 8-439	Error	module 'cordic_0' not found
Common 17-83	Information	Releasing license: Synthesis
Common 17-69	Error	Command failed: Synthesis failed - please see the console or run log file for details
Common 17-206	Information	Exiting Vivado at Sun Feb 5 09:06:00 2023...
fifo_generator_0_synth_1		
Vivado_Tcl 4-113	Status	Command: synth_design -top fifo_generator_0 -part xcau25p-ffvb676-2-e -incremental_mode off -mode out_of_context
Vivado_Tcl 4-321	Status	Starting synth_design
Common 17-347	Status	Attempting to get a license for feature 'Synthesis' and/or device 'xcau25p'
Vivado_Tcl 4-41	Status	7 Infos, 1 Warnings, 0 Critical Warnings and 2 Errors encountered.

Vivado_Tcl 4-43	Status	synth_design failed
Vivado_Tcl 4-391	Warning	The following IPs are missing output products for Synthesis target. These output products could be required for synthesis, please generate the output products using the generate_target or synth_ip command before running synth_design. C:/Users/chira/Desktop/DAC/SignalGenerator/HDL/ip/fifo_generator_0/fifo_generator_0.xci
IP_Flow 19-2162	Warning	IP 'fifo_generator_0' is locked: * IP definition 'FIFO Generator (13.2)' for IP 'fifo_generator_0' (customized with software release 2017.4.1) has a different revision in the IP Catalog. * Current project part 'xcau25p-ffvb676-2-e' and the part 'xc7a75tfgg484-1' used to customize the IP 'fifo_generator_0' do not match.
Common 17-349	Information	Got license for feature 'Synthesis' and/or device 'xcau25p'
Device 21-403	Information	Loading part xcau25p-ffvb676-2-e
Synth 8-7079	Information	Multithreading enabled for synth_design using a maximum of 2 processes.
Synth 8-7078	Information	Launching helper process for spawning children vivado processes
Synth 8-7075	Information	Helper process launched with PID 16984
Synth 8-439	Error	module 'fifo_generator_0' not found
Common 17-83	Information	Releasing license: Synthesis
Common 17-69	Error	Command failed: Synthesis failed - please see the console or run log file for details
Common 17-206	Information	Exiting Vivado at Sun Feb 5 09:06:00 2023...